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SEMESTER-II

UNIVERSITY OF DELHI

CNC-II/093/1(23)/2022-23/

Dated: 14.03.2023

NOTIFICATION

Sub: Amendment to Ordinance V

[E.C Resolution No. 38-1/ (38-1-5) dated 08.12.2022]

Following addition be made to Appendix-II-A to the Ordinance V (2-A) of the Ordinances of the University;

Add the following:

Syllabi of Semester-II of the following departments under Faculty of Interdisciplinary and Applied Sciences based on Under Graduate Curriculum Framework -2022 to be implemented from the Academic Year 2022-23.

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Category I

BSc. (Hons.) Electronics

DISCIPLINE SPECIFIC CORE COURSE-4 (DSC-4) – : Basic Instrumentation and Measurement Techniques

CREDIT DISTRIBUTION, ELIGIBILITY AND PRE-REQUISITES OF THE COURSE

Course title & Code	Credits	Credit distribution of the course			Eligibility criteria	Pre-requisite of the course (if any)
		Lecture	Tutorial	Practical/ Practice		
Basic Instrumentation and Measurement Techniques	4	3	0	1	Class 12 th Pass with PCM or Physics, Comp. Sc. & Maths.	Nil

Learning Objectives

The Learning Objectives of this course are as follows:

The objective of this subject is to provide insight into electronic instruments being used in the industries and labs. It details the basic working and use of different instruments used for measuring various physical quantities. Also, it details the identification, classification, construction, working principle and applications of various transducers used for displacement, temperature, pressure and intensity measurement.

Learning outcomes

After completion of the course, students will be able to-

Describe the working principle of different measuring instruments.

Choose appropriate measuring instruments for measuring various parameters in their laboratory courses.

Understand the significance of different measuring instruments including oscilloscopes.

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UNIT – I Fundamentals of Electronic Measurements (12 Hours)

Qualities of Measurement: SI system of units. Specifications of instruments, their static and dynamic characteristics. Error (Gross error, systematic error, absolute error and relative error) and uncertainty analysis.

Basic Measurement Instruments: PMMC instrument, galvanometer, DC measurement - ammeter, voltmeter, ohm meter, AC measurement (rectifier type, electro dynamo meter), Watt meter. Digital voltmeter systems (integrating and non-integrating types), digital multimeter,

Connectors and Probes: low capacitance probes, high voltage probes, current probes, identifying electronic connectors – audio and video, RF/Coaxial, USB etc.

UNIT – II Impedance Measurement and Power Supplies (12 Hours)

Measurement of Resistance and Impedance: Low Resistance: Kelvin's bridge method, Medium Resistance by Wheatstone bridge method, High Resistance by Megger. A.C. bridges, Measurement of Self Inductance, Anderson's bridge, Measurement of Capacitance, De Sauty's bridge, Measurement of frequency, Wien's bridge.

Regulated Power Supplies: Power Supply characteristics, Fixed power supply (78XX based), Dual power supplies (78XX and 79XX based), Variable power supply (LM317 based), current limiting, short-circuit shut down. Introduction of switch mode power supply (SMPS)

UNIT – III Oscilloscopes and Signal Generators (12 Hours)

Electronic Displays: The Cathode Ray Oscilloscope (CRO): Block diagram of a General Purpose Oscilloscope and its basic operation. Measurement of voltage, frequency and phase by oscilloscope. Oscilloscope probes. Sampling Oscilloscope. Digital storage oscilloscope (DSO), advantages and applications, Oscilloscope specifications (bandwidth, sensitivity, rise time).

Signal Generators: Types of generators and their operation: Audio oscillator, Function generators, Pulse generators, RF generators, Random noise generators.

UNIT – IV Transducers and Sensors (09 Hours)

Transducers and sensors: Classification of transducers, Basic requirement/characteristics of transducers, active & passive transducers, Resistive (Potentiometer, Strain gauge – Theory, types, temperature compensation and applications), Capacitive (Variable Area, air gap and permittivity Type), Inductive (LVDT) and piezoelectric transducers. Measurement of displacement, Measurement of temperature (RTD, thermistor, thermocouple, semiconductor IC sensors), Light transducers (photoresistors, photovoltaic cells, photodiodes).

Practical component (if any) – Basic Instrumentation and Measurement Techniques Lab – 30 Hours

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1. Design of ammeter and voltmeter using galvanometer.
2. Measurement of resistance by Wheatstone bridge.
3. Measurement of Capacitance by De Sauty's bridge.
4. Measurement of Inductance by Anderson's bridge.
5. To determine the characteristics of resistance transducer - Strain Gauge.
6. To determine the characteristics of an LVDT.
7. To study the variations of thermo-emf of a thermocouple. (Type J/Type K)
8. To study the I-V characteristics of Solar Cell.
9. To study the Characteristics of LDR, Photodiode
(i) Variable Illumination (ii) Linear Displacement.
10. Characteristics of one Solid State sensor/ Fiber optic sensor.

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than eight.

Essential/recommended readings

1. H. S. Kalsi, Electronic Instrumentation, 3rd Edition, Tata Mcgraw Hill, (2006).
2. W.D. Cooper and A. D. Helfrick, Electronic Instrumentation and Measurement Techniques, Prentice Hall (2005).
3. Joseph J Carr, Elements of Electronic Instrumentation and Measurement, 3rd Edition, Pearson Education (2005).
4. David A. Bell, Electronic Instrumentation and Measurements, 3rd Edition, Oxford University Press (2013).
5. R. A. Witte, Electronic Test Instruments, Analog and Digital Measurements, 2nd Edition, Pearson Education (2004).
6. A. K. Sawhney, Electrical and Electronics Measurements and Instrumentation, Dhanpatrai and Sons (2007).
K. Lal Kishore, Electronic Measurements and Instrumentation, 1st edition, Pearson Education India (2009).

Note: Examination scheme and mode shall be as prescribed by the Examination Branch, University of Delhi, from time to time.

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DISCIPLINE SPECIFIC CORE COURSE – 5 (DSC-5): Digital Electronics

Credit distribution, Eligibility and Prerequisites of the Course

Course title & Code	Credits	Credit distribution of the course			Eligibility criteria	Pre-requisite of the course (if any)
		Lecture	Tutorial	Practical/ Practice		
Digital Electronics	4	3	0	1	Class 12 th Pass with PCM or Physics, Comp. Sc. & Maths.	Nil

Learning Objectives

The Learning Objectives of this course are as follows:

- To represent information in various number systems.
- To convert data from one number system to another and do various arithmetic operations.
- To analyze logic systems and to implement optimized combinational circuits using Karnaugh Map.
- To analyze and implement sequential circuits using state machines.
- To analyze various memories and programmable logic devices.
- To analyze and understand the working of data converters.

Learning outcomes

After completion of the course, students will be able to-

Understand the concept of the number system with emphasis on binary numbers, its algebra and minimization techniques.

Understand basic logic gates, concepts of Boolean algebra and techniques to reduce/simplify Boolean expressions.

Analyze and design combinational as well as sequential circuits.

Understand the concepts related to Memories and PLD's.

Understand the working of analog to digital converters, digital to analog converters.

SYLLABUS OF DSC- 5

UNIT – I Introduction to Digital Electronics (09 Hours)

Number System and Codes: Decimal, Binary, Hexadecimal and Octal number systems, base conversions, Binary, Octal and Hexadecimal arithmetic, Addition, subtraction by Complements (1's and 2's) method, Binary Multiplication by computer method, Signed

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numbers, Binary Codes (BCD, 84-2-1, excess-3, Gray) BCD addition, Error detecting/correcting code (Parity, Hamming).

Logic Gates and Boolean Algebra: Truth table and symbolic representation of logic gates and their implementation using Universal gates, Basic postulates and fundamental theorems of Boolean algebra.

UNIT – II Combinational Circuit Design (12 Hours)

Canonical and Standard forms, Standard representation of logic functions (SOP and POS), Simplification of Boolean functions (up to 5 variables) using (i) Kmap (ii) Tabulation method, Binary Adder, Binary subtractor, parallel adder/subtractor, BCD adder, Code convertors.

Encoder, Decoder, Multiplexer, Demultiplexer, Implementing logic functions with Decoder and multiplexer.

UNIT – III Sequential Circuits (12 Hours)

Sequential logic design: Latches and Flip flops, S-R, D, J-K, master slave, T Flip flops and their characteristic equation, Clocked and edge triggered Flip flops, conversion between flip flops, Shift Registers, Universal Shift register, Bidirectional Shift Register, Ring counter and Johnson counter, Counters (synchronous, asynchronous and modulo-N) and their timing sequence.

Synchronous Sequential circuit synthesis: State Tables, State Transition Diagrams, minimization, state assignments, realization with T, D and JK flip flops, Finite state machine- Mealy and Moore model

UNIT – IV Signal Conversion, Memories and Logic Families (12 Hours)

A-D and D-A Conversion: 4 bit binary weighted resistor type D-A conversion, circuit and working. Circuit of R-2R ladder. A-D conversion characteristics, successive approximation ADC. (Mention of relevant ICs for all).

Memories: ROM, PROM, EPROM, EEPROM, Bipolar RAM, static and dynamic RAM, Memory Expansion (Word size and Word Capacity).

Programmable Logic Devices: Combinational circuit Implementation using PROM, PLA and PAL.

Digital Logic families: Fan-in, Fan out, Noise Margin, Power Dissipation, Figure of merit, Speed power product, TTL and CMOS families and their comparison

Practical component (if any) - Digital Electronics Lab (*Hardware and Circuit Simulation Software*) – 30 Hours

1. To verify and design AND, OR, NOT, XOR and XNOR gates using NAND gates.
 2. To convert a Boolean expression into a logic gate circuit and assemble it using logic gate IC's.
 3. Design a Half and Full Adder.
 4. Design a Half and Full Subtractor.
 5. Design a seven segment display driver.
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6. Implement a Boolean function using 4 X 1 multiplexer.
7. To build a Flip- Flop Circuits using elementary gates. (RS, Clocked RS, D-type, JK, JK Master slave).
8. Design a SISO, SIPO shift register.
9. Design an asynchronous/ synchronous Up/Down counter using D/T/JK Flip-Flop.
10. Design a non sequential counter using D/T/JK Flip flop.
11. Design a R-2R DAC.
12. Design an ADC circuit using ADC0804.

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than ten.

Essential/recommended readings

1. M. Morris Mano, "Digital System Design," Pearson Education Asia.
2. Thomas L., "Flyod, Digital Fundamentals," Pearson Education Asia.
3. W. H. Gothmann, "Digital Electronics: An Introduction To Theory And Practice," Prentice Hall of India.
4. Millman & Grabel, "Microelectronics," Tata McGraw Hill.
5. Donald D. Givone, " Digital Principles and Design," Tata McGraw- Hill.
6. R. P. Jain, "Modern digital Electronics," Tata McGraw- Hill.

Note: Examination scheme and mode shall be as prescribed by the Examination Branch, University of Delhi, from time to time.

DISCIPLINE SPECIFIC CORE COURSE– 6 (DSC-6): Analog Electronics-I

Credit distribution, Eligibility and Pre-requisites of the Course

Course title & Code	Credits	Credit distribution of the course			Eligibility criteria	Pre-requisite of the course (if any)
		Lecture	Tutorial	Practical/ Practice		
Analog Electronics-I	4	3	0	1	Class 12 th Pass with PCM or Physics, Comp. Sc. & Maths.	Nil

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Learning Objectives

The Learning Objectives of this course are as follows:

- Understand diodes (pn diode and Zener diode) and its applications in clipping and clamping circuits, rectifiers and voltage regulation (using Zener diodes) and concept of Power Supply.
- Understand frequency response of BJT and MOSFET amplifiers.
- Understand the concept of feedback and design feedback amplifiers and oscillators.
- Understand different power amplifiers and single tuned amplifiers.

Learning outcomes

After completion of the course, students will be able to-
Illustrate about rectifiers, transistor and MOSFET amplifiers and its biasing. Also compare the performances of its low frequency models.
Describe the frequency response of MOSFET and BJT amplifiers.
Explain the concepts of feedback and construct feedback amplifiers and oscillators.
Summarizes the performance parameters of amplifiers with and without feedback

SYLLABUS OF DSC-6

UNIT – I Diode applications (09 Hours)

Diode Circuits: Ideal diode, piecewise linear equivalent circuit, dc load line, static and dynamic resistance, Quiescent (Q) point. Clipping and clamping circuits. Rectifiers: HWR, FWR (center tapped and bridge). Circuit diagrams, working and waveforms, ripple factor & efficiency, Voltage doubler
Filters: Circuit diagram and explanation of shunt capacitor filter with waveforms.
Voltage Regulator: Zener diode regulator circuit diagram and explanation for load and line regulation

UNIT – II BJT based Amplifiers and Oscillator (12 Hours)

Transistor: Input and Output Characteristics, Concept of Biasing and its significance, Concept of DC and AC analysis. Overview of Common Emitter BJT amplifier, Concept of Darlington pair

Power Amplifiers: Difference between voltage and power amplifier, classification of power amplifiers (Class A, Class B, Class AB, Class C, Class D), Concept of Class A single ended power amplifier, Transformer coupled Class A power amplifier and complementary symmetry Class B push pull power amplifier, overall efficiency, concept of crossover distortion, harmonic distortion and heat sinks.

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Feedback Amplifiers: Concept of feedback, negative and positive feedback, voltage (series and shunt), feedback amplifiers gain, input and output impedances. Barkhausen criterion for oscillations, RC phase shift oscillator

UNIT – III MOSFET Fundamentals (12 Hours)

MOSFET: Operation of n-channel and p-channel MOSFETs, Overview of Depletion and Enhancement MOSFET, Transfer Characteristics, Drain Characteristics, MOSFET as a switch. short channel effects, non-ideal effects in MOS transistors: the finite output resistance in the saturation region, the body effect, subthreshold conduction, breakdown effects, and temperature effects.

MOSFET DC analysis: Biasing circuits- drain feedback, voltage divider, source feedback, bias stability, Graphical analysis, load line.

UNIT – IV MOSFET based Amplifiers (12 Hours)

MOSFET AC analysis: AC equivalent circuit of MOSFET, MOSFET parameters,

MOSFET Amplifiers: circuit and small signal model of Common Source amplifier, small signal parameters: input resistance, output resistance and voltage gain, circuits of Common Drain and Common Gate configurations. Comparison of BJT based (CE, CB and CC) and MOSFET based (CS, CD, CG) - Qualitative only.

Multistage MOSFET circuits: Cascaded circuits and Cascode circuits, effect of multistage circuits on gain and bandwidth.

MOSFET Application circuits: CMOS as inverter circuit, depletion mode n-MOSFET and p-MOSFET as load device

Practical component (if any) - Analog Electronics-I Lab – 30 Hours

(Hardware and Circuit Simulation Software)

1. Study of the half wave or full wave rectifier
2. Study of Zener diode as voltage regulator.
3. Study of any two types of
 - (a) clipping circuits
 - (b) clamping circuits.
4. Study of a Single Stage CE amplifier.
5. Study of Class A or Class B Power Amplifiers.
6. Study of Voltage divider bias for MOSFET
7. Study of the frequency response of Common Source MOSFET amplifier.
8. Study of MOSFET based Phase Shift Oscillator

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Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than seven.

Essential/recommended readings

1. Electronic Devices and circuit theory, Robert Boylestad and Louis Nashelsky, 9th Edition, 2013, PHI
2. Donald A. Neamen, Electronic Circuit Analysis and Design, Tata McGraw Hill (2002)
3. Electronic devices, David A Bell, Reston Publishing Company
4. Giovanni Saggio, Principles of Analog Electronics, CRC Press (2014)
5. D. L. Schilling and C. Belove, Electronic Circuits: Discrete and Integrated, Tata McGraw Hill (2002)
6. J. Millman and C. C. Halkias, Integrated Electronics, Tata McGraw Hill (2001)
7. J. J. Cathey, 2000 Solved Problems in Electronics, Schaum's outline Series, Tata McGraw

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Category-IV

Pool of Generic Electives offered by Department of Electronic Science

GENERIC ELECTIVES (GE-2A): Digital System Design

Credit distribution, Eligibility and Pre-requisites of the Course

Course title & Code	Credits	Credit distribution of the course			Eligibility criteria	Pre-requisite of the course	Department offering the course
		Lecture	Tutorial	Practical/ Practice			
Digital System Design	4	3	0	1	Class 12 th Pass with PCM or Physics, Comp. Sc. & Maths.	Nil	Electronic Science

Learning Objectives

In addition to familiarization with the combinational and sequential circuits, students will be adept in using simulation of digital circuits on software, which is in high demand, for designing combinational or sequential circuits. As there are lot of industrial and research-based job opening in the area, the course offers a hands-on in designing digital systems on hardware and testing with a holistic approach to the subject, making students ready for the industry or research

Learning outcomes

After completion of the course, students will be able to-

- Understand and represent numbers in powers of base and concepts of Boolean algebra.
- Understand basic logic gates and minimization techniques.
- Analyze and design combinatorial circuits.
- Analyze and design sequential circuits.

SYLLABUS

UNIT – I Number Systems and Boolean Algebra (09 Hours)

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Number System and Boolean algebra: Decimal, Binary, Hexadecimal, Octal, BCD, Conversions, Complements (1's and 2's), Signed and unsigned numbers, addition and subtraction, Gray Code. Boolean algebra- Positive and negative logic. Boolean laws, De Morgan's theorems, simplification of Boolean expressions-SOP and POS

UNIT – II Logic Gates and Minimization (12 Hours)

Logic gates and Karnaugh map: Logic gates- basic logic gates-AND, OR, NOT, logic symbol and truth table. Derived logic gates (NAND, NOR, XOR & XNOR). Universal property of NOR and NAND gates. K-map minimization of 3 and 4 variable functions/expressions.

UNIT – III Combinational Circuits (12 Hours)

Combinational logic analysis and design: Multiplexers and Demultiplexers, Adder (half and full), Subtractor (half and full), Parallel adder/subtractor, Encoder and Decoder, Understanding VHDL program of a Full Adder and 3 to 8 decoder

UNIT – IV Flip Flops and Counters (12 Hours)

Sequential logic design: Latch, Flip flop, S-R FF, J-K FF, T and D type FFs, clocked FFs, registers, Counters (synchronous and asynchronous, ring and Johnson)

Practical component (if any) - Digital System Design Lab – 30 Hours

(Hardware and Circuit Simulation Software)

To verify and design AND, OR, NOT and XOR gates using NAND gates.

2. Design a Half and Full Adder.
3. Design a Half and Full Subtractor.
4. Implement Boolean functions using 8X1 and 16X1 Multiplexers.
5. Implement Boolean functions using decoder.
6. Implement an encoder.
7. Study of counters using dedicated counter ICs.
8. Study of registers (SISO, SIPO, PISO and PIPO) using universal shift register IC.

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than seven.

Essential/recommended readings

1. M. Morris Mano Digital System Design, Pearson Education Asia, (Fourth Edition)
2. Thomas L. Floyd, Digital Fundamentals, Pearson Education Asia (1994)
3. W. H. Gothmann, Digital Electronics: An Introduction To Theory And Practice, Prentice Hall of India (2000)
4. R. L. Tokheim, Digital Principles, Schaum's Outline Series, Tata McGraw- Hill (1994)

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GENERIC ELECTIVES (GE-2B): Data Visualization Techniques

Credit distribution, Eligibility and Pre-requisites of the Course

Course title & Code	Credits	Credit distribution of the course			Eligibility criteria	Pre-requisite of the course	Department offering the course
		Lecture	Tutorial	Practical/ Practice			
Data Visualization Techniques	4	3	0	1	Class XII Passed with Maths	Basic Knowledge of Python Programming Language	Electronic Science

Learning Objectives

This course is all about data visualization, the art and science of turning data into readable graphics. It enables the students to design and create data visualizations based on data available and tasks to be achieved. This process includes data modeling, data processing (such as aggregation and filtering), mapping data attributes to graphical attributes, and strategic visual encoding based on known properties of visual perception as well as the task(s) at hand. Students will also learn to evaluate the effectiveness of visualization designs, and think critically about each design decision, such as choice of color and choice of visual encoding. Students will create their own data visualizations, and learn to use Open-Source data visualization tools.

Learning outcomes

After completion of the course, students will be able to-

- Design and create data visualizations.
- Conduct exploratory data analysis using visualization.
- Craft visual presentations of data for effective communication.
- Use knowledge of perception and cognition to evaluate visualization design alternatives.
- Design and evaluate color palettes for visualization based on principles of perception.
- Apply data transformations such as aggregation and filtering for visualization.
- Identify opportunities for application of data visualization in various domains.

Tools Required: Open-source Visualization tools, Python, Plotly, Tableau

SYLLABUS

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UNIT – I Understanding Data Visualization (09 Hours)

Introduction to Data Visualization, Various tools for Data Visualization. Introduction to Numpy, Pandas and Matplotlib. Structured & Semi-structured Dataset, Data Cleaning and Preparation. Handling Missing Data, Data Transformation. Basic Plotting with Matplotlib, Dataset on Immigration e.g. Canada (source: <https://open.canada.ca/>) any other. Univariate and Multivariate Visualization. Introduction to cloud computing.

UNIT – II Data Visualization Techniques (12 Hours)

Data Visualizations Techniques: Line Plots, Area Plots, Histograms, Bar Charts, Pie Charts, Box Plots, Scatter Plots, Bubble Plots, Waffle Charts, Word Clouds, Seaborn and Regression Plots, Creating Maps and Visualizing Geospatial Data - Introduction to Folium, Maps with Markers, Choropleth Maps.

UNIT – III Creating Dashboards with Plotly (12 Hours)

Introduction to Seaborn, Basic plotting with Seaborn. Introduction to Plotly. Scatter chart, Bubble Plot, Pie chart, Gantt chart, Contour plotting, Sunburst and Polar charts, Heatmaps.

UNIT – IV Data Visualization using Tableau (12 Hours)

Introduction to Tableau Desktop, connecting to dataset, Data preparation, Filtering and sorting data, Creating basic chart types (bar charts, line charts etc.), Assembling a dashboard layout, Using dashboard filters, Transform the data, Simple calculations in Tableau, Creating advanced chart types. Introduction to Data Story.

Practical component (if any) - Data Visualization Techniques Lab – 30 Hours

(Perform practical on Dataset available at Kaggle / Github / UCI Machine Learning Repository)

1. Visualization of Spreadsheet Models.
2. Visualization of Semi-Structured Data.
3. Interactive Plots in Python and Tableau.
4. Hierarchical and Topographical Data Visualizations in Tableau.
5. Calendar Heatmaps and Flow Data Visualizations in Python.
6. Time Series Data Visualization in Plotly.
7. Creating cloud account Amazon/Azure/Google/IBM to store images /files / programs.
8. Use a dataset that contains immigration details e.g. Canada for a given duration of 30 years (Canada Immigration Dataset, source: <https://open.canada.ca/>) or any other
 - a. Create an area plot for top 6 immigrant countries in a given duration.
 - b. Create and year-wise immigrant bar chart from India to Canada in a given duration.
 - c. Create a boxplot of immigrants for three given countries.

This document is prepared from the following University Notifications

- https://www.du.ac.in/uploads/new-web/15092023_Indis_sem1.pdf
 - https://www.du.ac.in/uploads/new-web/notifications-2021/28032023_nep-Faculty%20of%20Interdisciplinary%20&%20Applied%20Sciences.pdf
 - https://www.du.ac.in/uploads/new-web/15092023_Indis_sem3.pdf
 - https://www.du.ac.in/uploads/new-web/18092023_Inter_4.pdf
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d. Show the total no. of immigrants using Area Chart and Pie chart for two given countries.

e. Create a scatter Histogram for the immigrants in the given year for two specific countries.

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than seven.

Essential/recommended readings

1. Data Visualization with Python for Beginners: Visualize Your Data using Pandas, Matplotlib and Seaborn by AI Publishing. ISBN: 1733042680-978
2. Learn and Practice Data Visualization using Python by Swapnil Saurav, Eka Publishers. ISBN: 8194633426-978
3. Python Data Science Handbook by Jake VanderPlas, Shroff/O'Reilly. ISBN: -978 9352134915
4. Data Visualization with Tableau by Praveen Kumar, Gurucool Publishing. ISBN: 8194746997-978
5. Interactive Dashboards and Data Apps with Plotly and Dash by Elias Dabbas, Packt Publishing Limited. ISBN: 1800568914-978

Suggestive readings -

1. Python Data Science Handbook by Jake VanderPlas, Shroff/O'Reilly. ISBN: 9352134915-978
2. Data Science from Scratch: First Principles with Python by Joel Grus, Shroff/O'Reilly. ISBN: 9352138326-978

Note: Examination scheme and mode shall be as prescribed by the Examination Branch, University of Delhi, from time to time.